

Fig.1

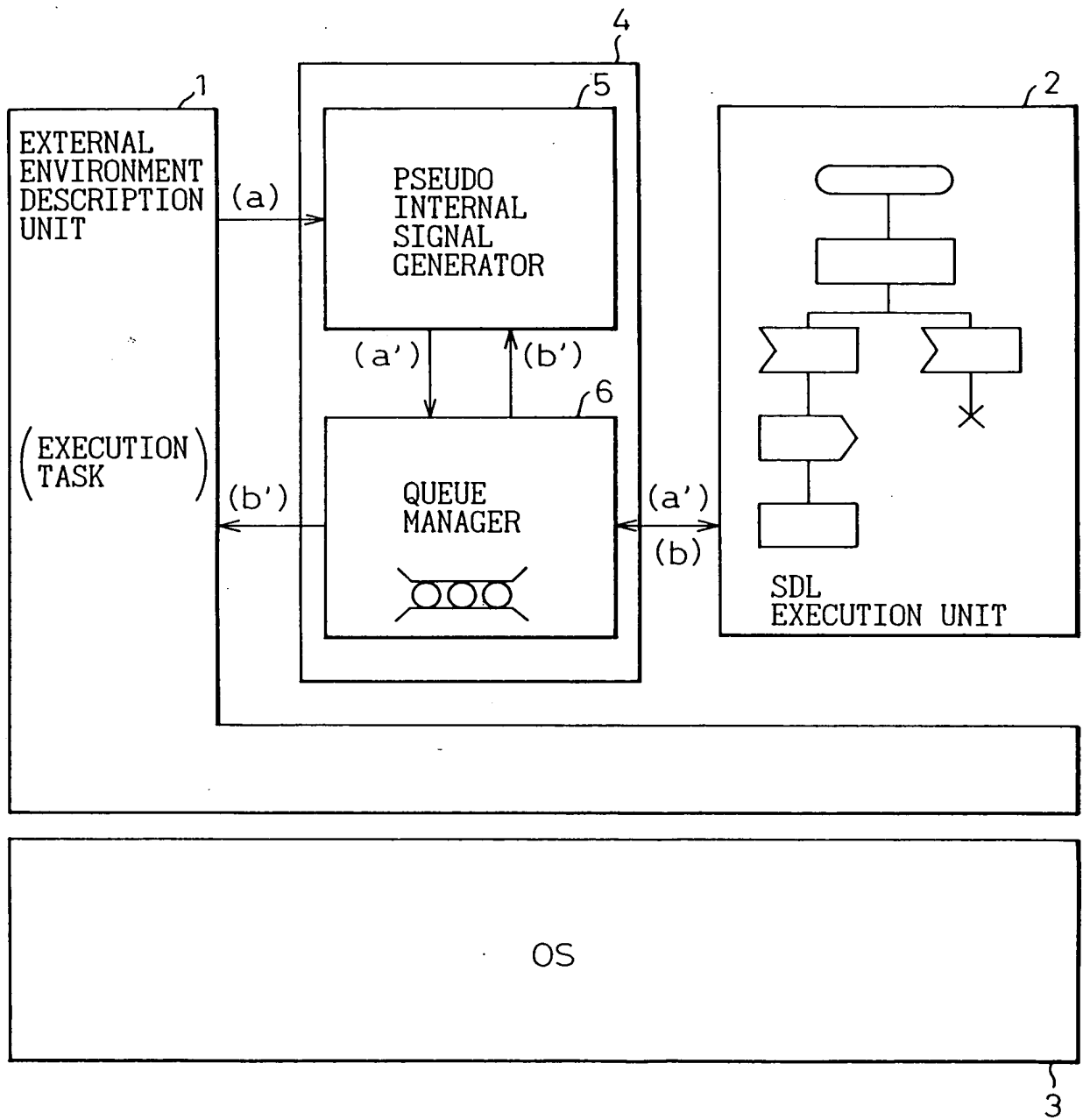




Fig.3

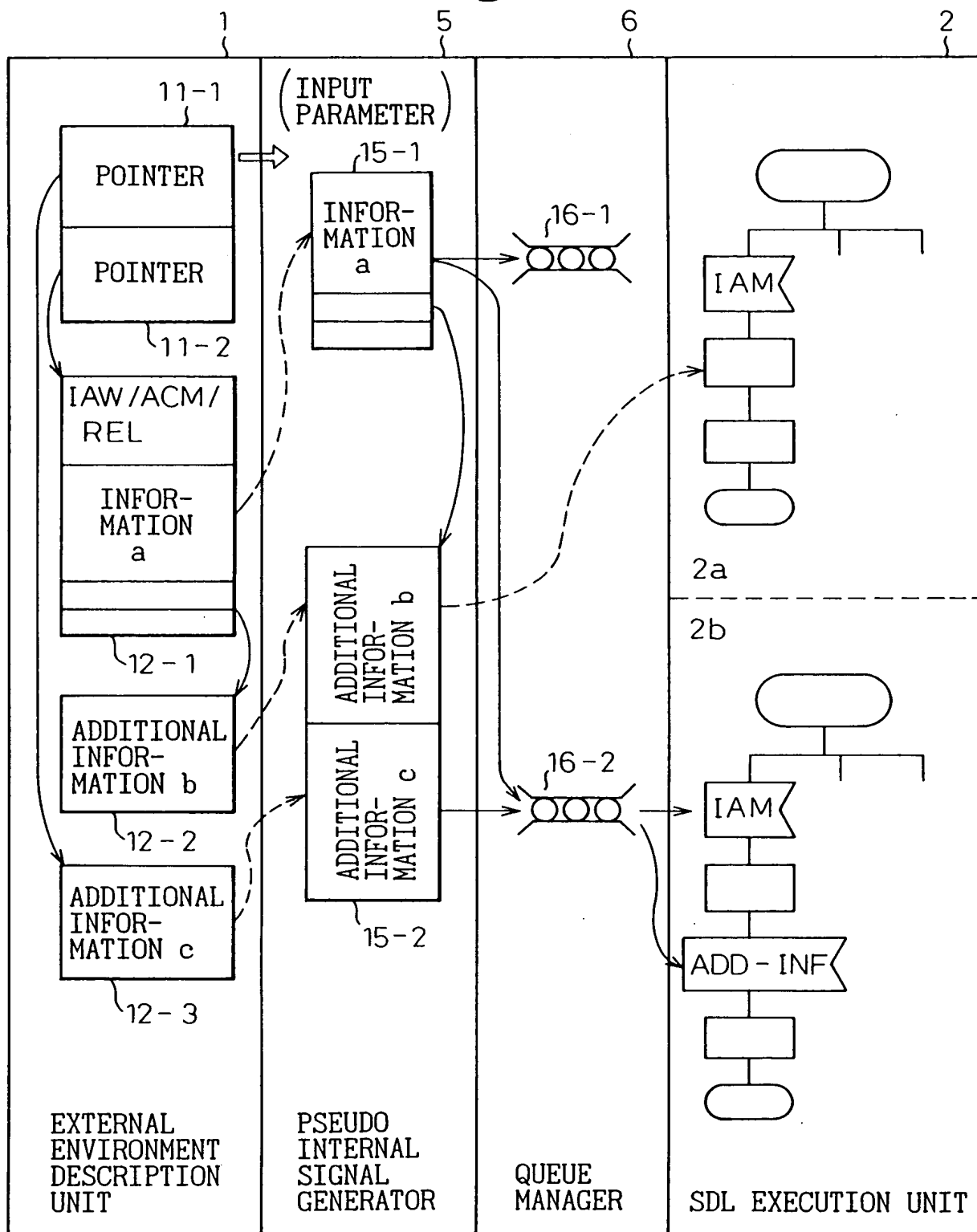


Fig.4

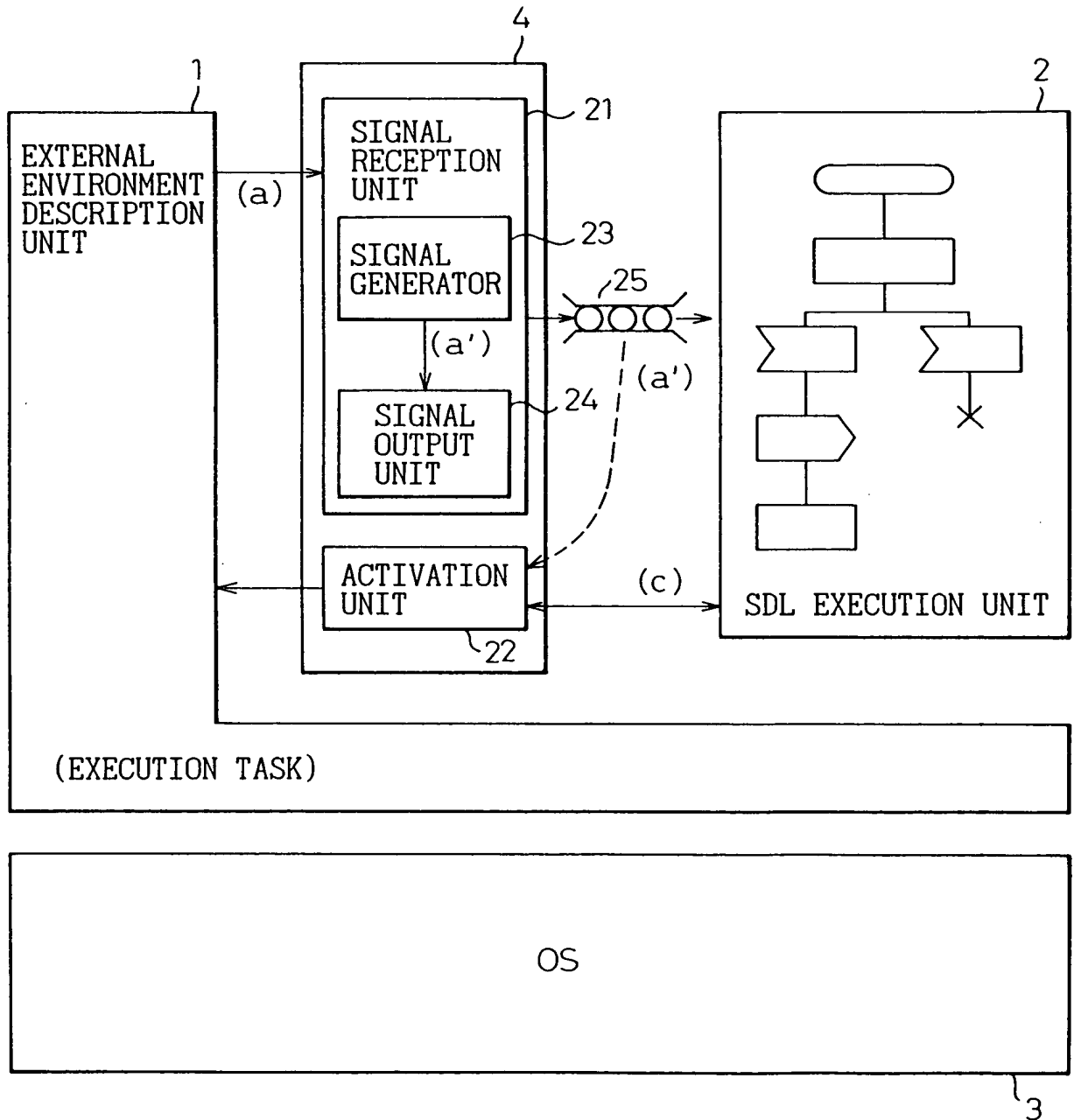


Fig.5

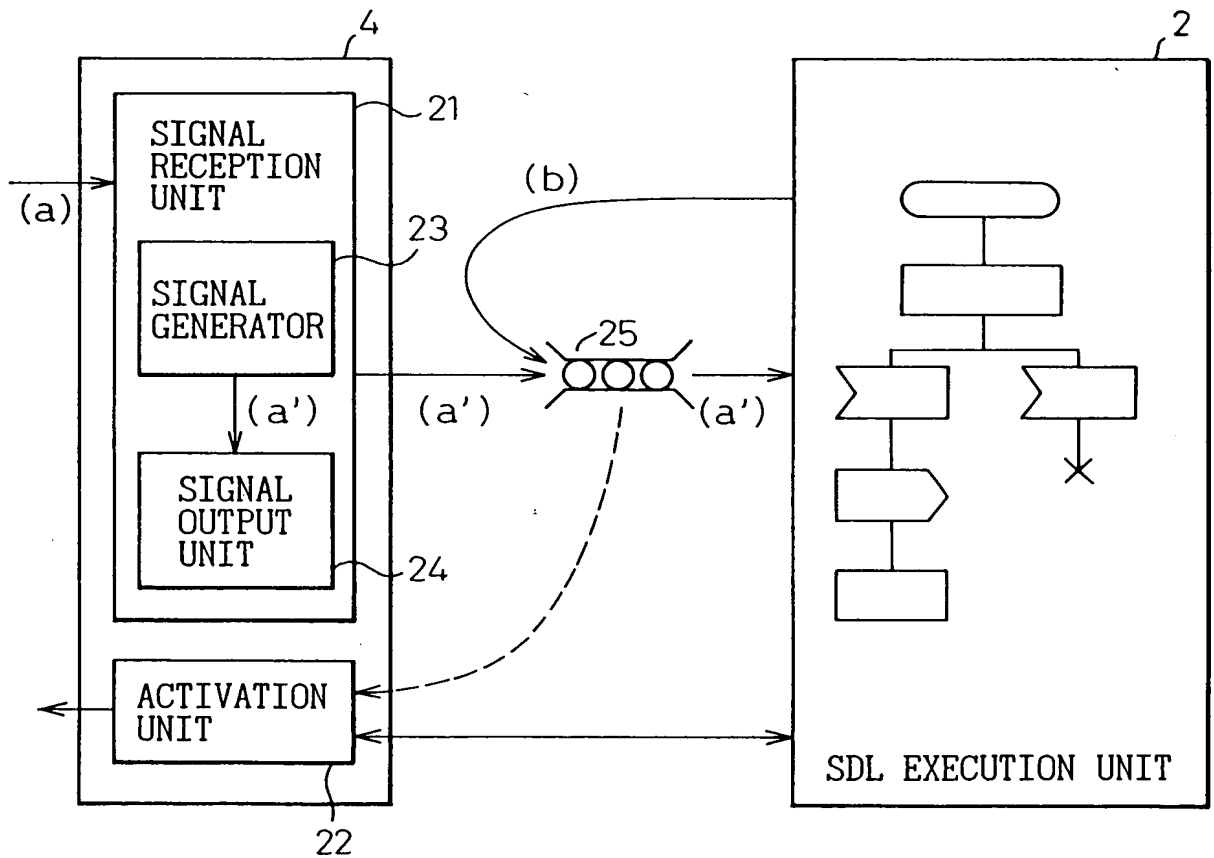


Fig.6

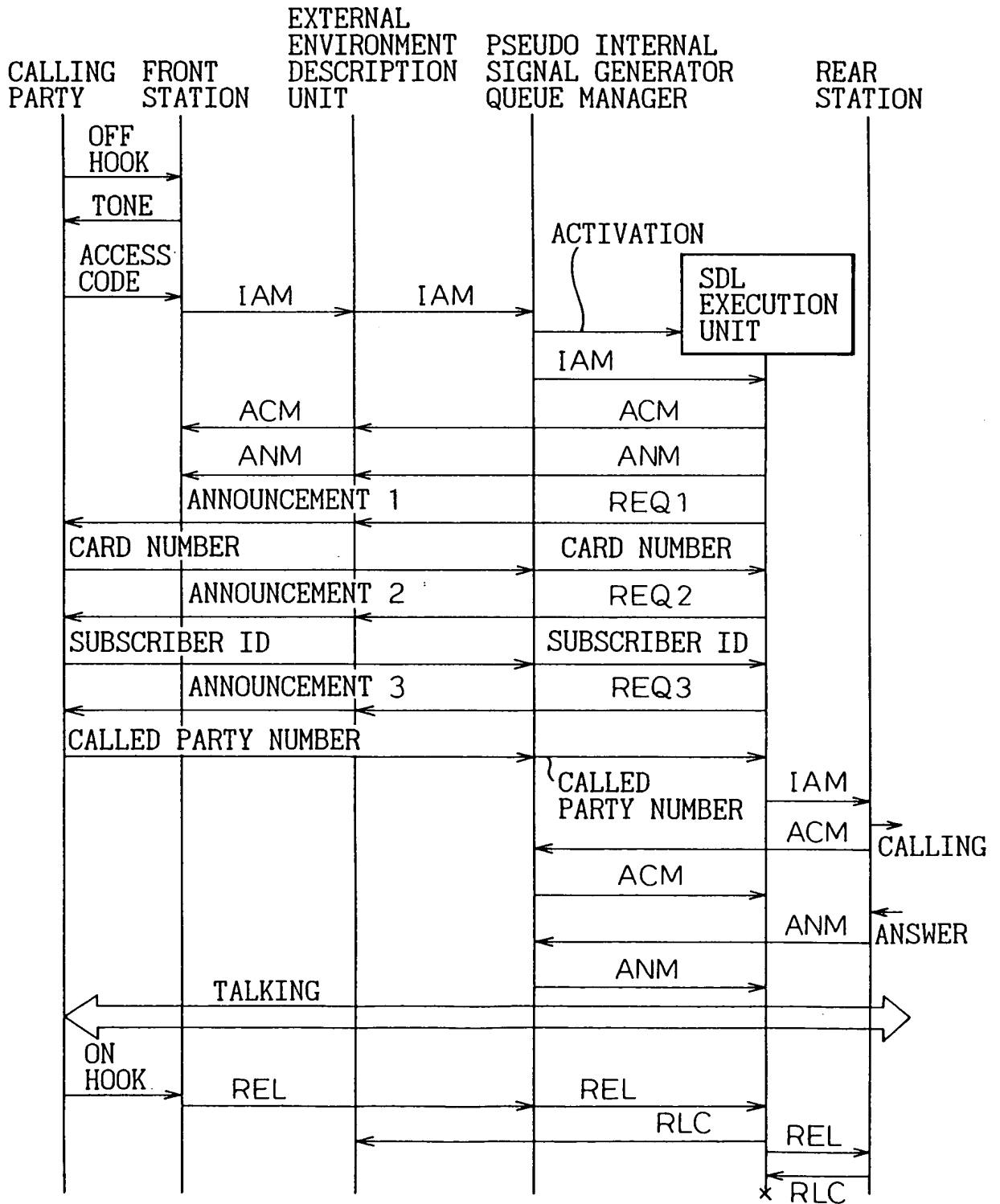


Fig.7

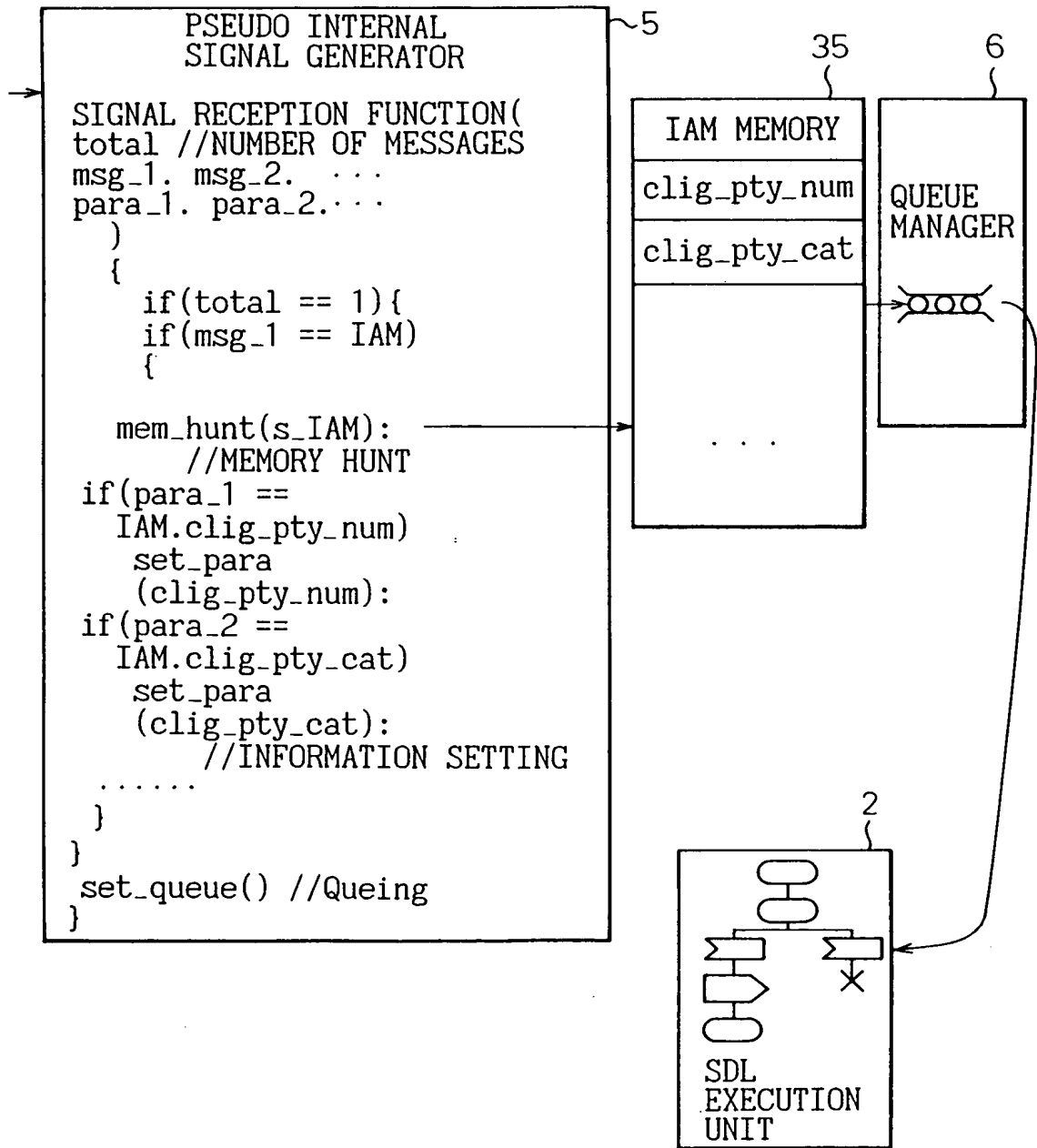
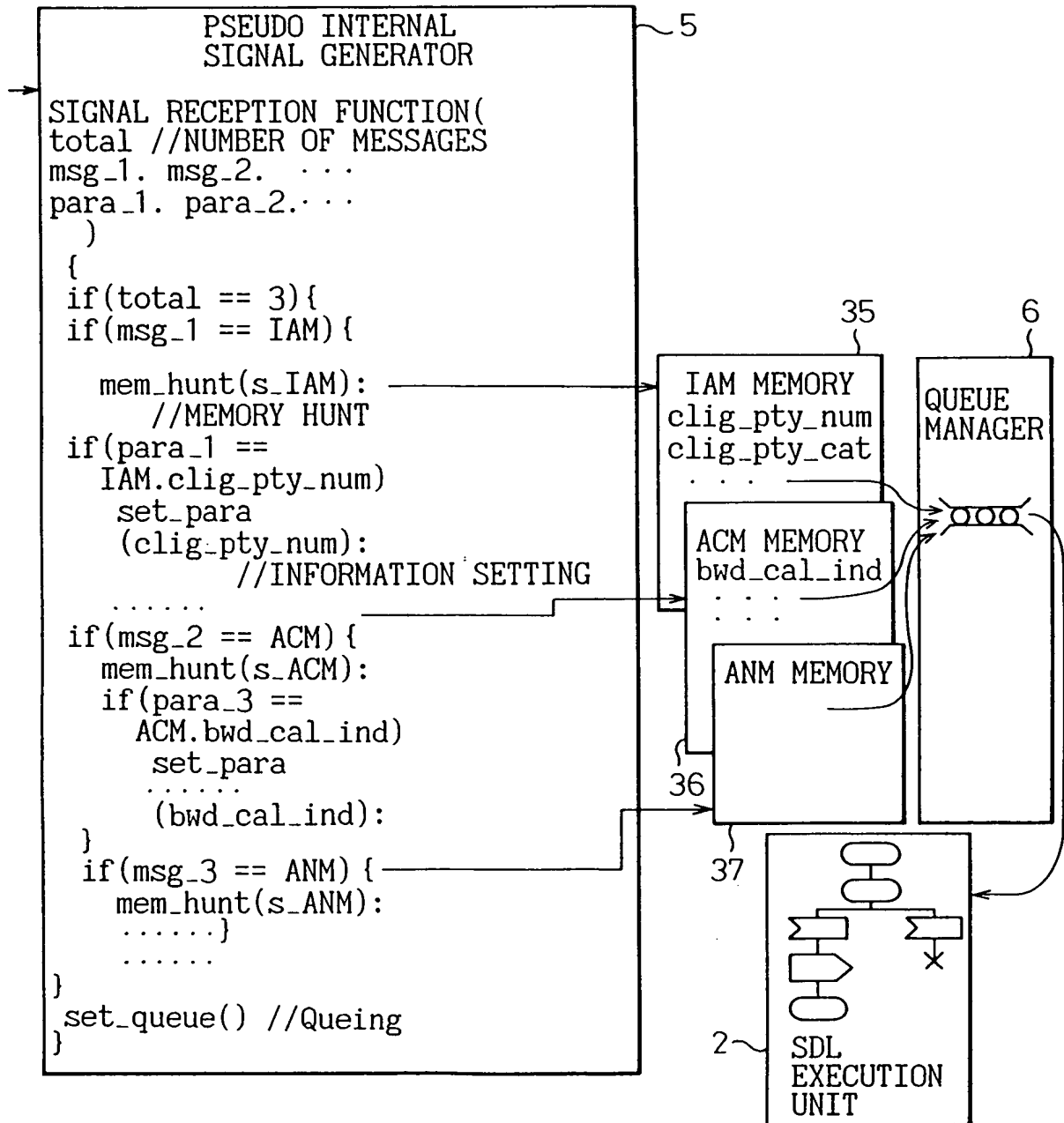


Fig.8



```

PSEUDO INTERNAL SIGNAL GENERATOR
SIGNAL RECEPTION FUNCTION(
total //NUMBER OF MESSAGES
msg_1. msg_2. . . .
para_1. para_2. . . .
end_of msg //0:NO
1:YES
){
  if(total == 1){
    if(msg_1 == IAM){
      mem_hunt(s_IAM):
      if(para_1 == IAM.clig_pty_num)
        set_para
        (clig_pty_num):
    }
  }
}
if(end_of_msg == 0){
  wait(next_IAM):
  //NO QUEUING TILL
  NEXT IAM RECEPTION
}

SIGNAL RECEPTION FUNCTION(
total //NUMBER OF MESSAGES
msg_1. msg_2. . . .
para_1. para_2. . . .
end_of_msg //0:NO
1:YES
){
  if(total == 1){
    if(msg_1 == IAM){
      mem_hunt(s_IAM):
      if(para_1 == IAM.clig_pty_cat)
    }
  }
}
if(end_of_msg==1){
  set_queue():
}

```

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IAM MEMORY

clig\_pty\_num

clig\_pty\_cat

...

QUEUE MANAGER

2

SDL EXECUTION UNIT

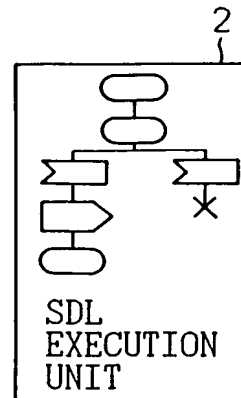


Fig.10

PRIOR ART

